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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Boucher et al. Ser. No: 10/634,062  
Filing Date: August 4, 2003 Examiner: Unknown  
Atty. Docket No: ALA-008G GAU: Unknown

For: METHOD AND APPARATUS FOR DATA RE-ASSEMBLY WITH A HIGH  
PERFORMANCE NETWORK INTERFACE

July 23, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Supplemental Information Disclosure Statement

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring one hundred and thirty-seven documents listed on an enclosed nine-page form PTO-1449 to the attention of the Examiner in the above-identified application.

Citation of these documents shall not be construed as an admission that the documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Copies of the documents listed on the enclosed one-page form PTO-1449 are not submitted, with the exception of one document, because they were submitted in an earlier application (10/005,536) which is relied upon for an earlier filing date under 35 U.S.C. §120.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 23, 2007.

Date: 7-23-07

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Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 10/634,062

4<sup>th</sup> INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Filing date: August 4, 2003

Inventors: Laurence B. Boucher et al.

Group Art Unit: Unknown

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	1	4,485,455	November 27, 1984	Gary W. Boone et al	364	900	
	2	4,485,460	November 27, 1984	Mark A. Stambaugh	365	203	
	3	4,700,185	October 13, 1987	Thomas J. Balph et al	340	825.5	
	4	5,418,912	May 23, 1995	David A. Christenson	395	200	
	5	5,535,375	July 9, 1996	Eshel et al.	391	500	
	6	5,574,919	November 12, 1996	Arun N. Netravali et al	395	561	
	7	5,566,170	October 15, 1996	Bakke et al.	370	60	
	8	5,598,410	January 28, 1997	Stone	370	469	
	9	5,633,780	May 27, 1997	Cronin et al.	361	220	
	10	5,682,534	October 28, 1997	Kapoor et al.	395	684	
	11	5,699,350	December 16, 1997	Andrew J. Kraslavsky	370	254	
	12	5,742,765	April 21, 1998	Wong et al.	395	200	
	13	5,768,618	June 16, 1998	Gene R. Erickson et al	395	829	
	14	5,778,419	July 7, 1998	Hansen et al.	711	112	
	15	5,828,835	October 27, 1998	Mark S. Isfeld et al	395	200.3	
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Examiner /Jerry Dennison/

Date Considered 01/20/2009

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<b>4<sup>th</sup> INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	Filing date: August 4, 2003
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	18	5,892,903	April 6, 1999	Christopher W. Klaus	395	187.01	
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	20	5,987,022	November 16, 1999	Robert L. Geiger et al	370	349	
	21	5,996,013	November 30, 1999	Gary Scott Delp et al	709	226	
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	23	6,041,381	March 21, 2000	Geoffrey B. Hoese	710	129	
	24	6,049,528	April 11, 2000	Ariel Hendel et al	370	235	
	25	6,067,569	May 23, 2000	Mohamed J. Khaki et al	709	224	
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	27	6,097,734	August 1, 2000	Joel Gotesman et al	370	474	
	28	6,111,673	August 29, 2000	Gee-Kung Chang et al	359	123	
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	30	6,122,670	September 19, 2000	Toby D. Bennett et al	709	236	
	31	6,141,701	October 31, 2000	Mark M. Whitney	710	5	
	32	6,181,705	February 24, 2004	San-Hong Kim	370	392	
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	36	6,343,360	January 29, 2002	David Feinleib	713	1	
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	46	6,502,144	December 31, 2002	Jean-Paul Accarie	710	8	
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	48	6,570,884	May 27, 2003	Glenn William Connery et al	370	419	
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	52	6,657,757	December 2, 2003	Gee-Kung Chang et al	359	124	

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	65	2003/0110344	June 1, 2003	Szezepanek et al.	711	100	
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	75	WO 00/13091	March 9, 2000	PCT/US98/24943			
	76	WO 01/04770	January 18, 2001	PCT/US00/18939			
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	83	Internet pages entitled "Hardware Assisted Protocol Processing", (which Eugene Feinber is working on), 1 page, printed 11/25/98.
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103	EETimes article entitled "Tuner for Toshiba, Toshiba Taps iReady for Internet Tuner", by David Lammers, 2 pages, printed 11/02/98.
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109	"Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, December 1998, 17 pages.

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127	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
128	Article from Rice University entitled "LRP: A New Network Subsystem Architecture for Server Systems", by Peter Druschel and Gaurav Banga, 14 pages.
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